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Applicant(s): Herbert L. Ho, et al. **Examiner:** Matthew E. Warren
Serial No: 10/787,002 **Art Unit:** 2815
Filed: February 25, 2004 **Docket:** YOR920030635US1 (17343)
Dated: November 6, 2006

For: ULTRA-THIN SOI VERTICAL BIPOLAR TRANSISTORS WITH
AN INVERSION COLLECTOR ON THIN-BURIED OXIDE (BOX)
FOR LOW SUBSTRATE-BIAS OPERATION AND METHODS
THEREOF

Confirmation No. 3930

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Commissioner for Patents
United States Patent Office
Alexandria, VA 23313-1450

RESPONSE UNDER 37 C.F.R. § 1.116

Sir:

In response to the Office Action dated August 9, 2006, applicants submit the following amendments and remarks for entry of record in the above-identified patent application.

CERTIFICATION OF ELECTRONIC TRANSMISSION

I hereby certify that this document is being filed in the United States Patent and Trademark Office on the date shown below via electronic transmission.

Dated: November 6, 2006


Leslie S. Szivos, Ph.D.